

|   | Type | L # | Hits | Search Text   | DBs   | Time Stamp           | Comments |
|---|------|-----|------|---|---|----------------------|----------|
| 1 | BRS  | L1  | 112  | mem\$1 adj1 switch\$3<br>and (wave or bulk or<br>filter\$3) and<br>substrate                                  | USPA<br>T   | 2003/02/2<br>5 09:24 |          |
| 2 | BRS  | L2  | 125  | mem\$1 adj1 switch\$3<br>and (wave or bulk or<br>filter\$3) and<br>substrate                                  | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:27 |          |
| 3 | BRS  | L3  | 21   | (mem\$1 or<br>electromechanical or<br>electro adj1<br>mechanical) adj1<br>switch and tun\$4 adj2<br>filter\$3 | USPA<br>T   | 2003/02/2<br>5 09:34 |          |
| 4 | BRS  | L4  | 22   | (mem\$1 or<br>electromechanical or<br>electro adj1<br>mechanical) adj1<br>switch and tun\$4 adj2<br>filter\$3 | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:36 |          |
| 5 | BRS  | L5  | 1    | (micro adj1 switch or<br>microswitch) and<br>tun\$4 adj2 filter\$3  | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:37 |          |
| 6 | BRS  | L6  | 18   | (micro adj1 switch or<br>microswitch) and<br>tun\$4 adj2 filter\$3  | USPA<br>T   | 2003/02/2<br>5 09:37 |          |

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| 1 | BRS  | L1  | 112  | mem\$1 adj1 switch\$3<br>and (wave or bulk or<br>filter\$3) and<br>substrate                                      | USPA<br>T   | 2003/02/2<br>5 09:24 |          |
| 2 | BRS  | L2  | 125  | mem\$1 adj1 switch\$3<br>and (wave or bulk or<br>filter\$3) and<br>substrate                                      | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:27 |          |
| 3 | BRS  | L3  | 21   | (mem\$1 or<br>electromechanical or<br>electro adj1<br>mechanical) adj1<br>switch and tun\$4 adj2<br>filter\$3     | USPA<br>T   | 2003/02/2<br>5 09:34 |          |
| 4 | BRS  | L4  | 22   | (mem\$1 or<br>electromechanical or<br>electro adj1<br>mechanical) adj1<br>switch and tun\$4 adj2<br>filter\$3     | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:36 |          |
| 5 | BRS  | L5  | 1    | (micro adj1 switch or<br>microswitch) and<br>tun\$4 adj2 filter\$3  | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM<br>TDB | 2003/02/2<br>5 09:37 |          |
| 6 | BRS  | L6  | 18   | (micro adj1 switch or<br>microswitch) and<br>tun\$4 adj2 filter\$3  | USPA<br>T   | 2003/02/2<br>5 09:37 |          |
| 7 | BRS  | L7  | 596  | (microswitch or<br>(mem\$1 or micro))<br>adj1 switch\$2 and<br>input and output and<br>filter\$3                  | USPA<br>T   | 2003/02/2<br>5 09:42 |          |
| 8 | BRS  | L8  | 92   | (microswitch or<br>(mem\$1 or micro))<br>adj1 switch\$2 and<br>input and output and<br>filter\$3 and<br>substrate | USPA<br>T   | 2003/02/2<br>5 09:59 |          |

|   | Type | L # | Hits | Search Text   | DBs  | Time Stamp       | Comments |
|---|------|-----|------|---|--|------------------|----------|
| 9 | BRS  | L9  | 74   | (microswitch or<br>(mem\$1 or micro))<br>adj1 switch\$2 and<br>input and output and<br>filter\$3 and<br>substrate | US-P<br>GPUB<br>;<br>EPO;<br>JPO;<br>DERW<br>ENT;<br>IBM-<br>TDB | 2003/02/25 10:00 |          |